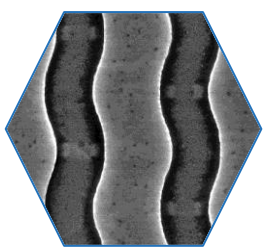


Automotive Low-Voltage Si MOSFET Comparison 2021

Exhaustive technology and cost comparisons of 11 AEC-Q101 Si MOSFETs from Infineon, ON Semiconductor, STMicroelectronics, Toshiba, Nexperia, Rohm, Diodes Incorporated, and Taiwan Semiconductor.



New environmental regulations targeting carbon neutrality by 2050 by some governments play in favor of faster vehicle electrification. Power electronics is a key technology for this transition. Power semiconductors of various voltage ranges can be found in any EV. Low voltage MOSFETs are already present in petrol-powered cars. The growing demand for all electrification types increases the need for DC/DC conversion from 48-12 V or 400-12 V. Moreover, more infotainment and communication will also favor the automotive-qualified MOSFET market.

Silicon MOSFETs benefit from mature infrastructure and processes. Meanwhile, new device generations are coming to the market. In addition to performance improvements, Si MOSFET die costs will be further reduced thanks to a 12-inch silicon wafer transition that will make their cost increasingly competitive.

System Plus Consulting presents an overview of the state of the art of 11 automotive-qualified AEC-Q101 Si MOSFETs of four voltage classes: 40 V, 50 V, 60 V, and 100 V. They are from eight manufacturers: Infineon, ON Semiconductor, STMicroelectronics, Toshiba, Nexperia, Rohm, Diodes Incorporated, and Taiwan Semiconductor. Some of these manufacturers have been highlighted during data analysis performed on systems analyzed in our Automotive

Tracks.

This report highlights differences in their technology parameters and design, and its impact on production cost. It provides detailed optical and SEM pictures from the device's opened-package down to the microscopic level of transistor design. The focus of the report is on chip technology, and hence the devices are analyzed and costs are simulated at wafer and die levels.

Finally, this report provides exhaustive physical, technological and manufacturing cost comparisons of the analyzed devices for the different voltage classes.

COMPLETE TEARDOWN WITH

- Detailed optical and SEM photos
- Precise dimensional measurements
- Manufacturing process flow of selected technology
- Supply chain evaluation
- Manufacturing wafer and die cost analysis
- Estimated bare die selling price
- Comparisons of technology design parameters and electric performances including figures of merit, current density, etc.
- Comparisons of cost including wafer cost, die cost, die cost per Ampere

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- Optical photos for package opening and die topsides and SEM photos for top-side delayered patterns and cross-sections.
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 - ✓ ON Semiconductor
 - ✓ STMicroelectronics
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 - ✓ ROHM
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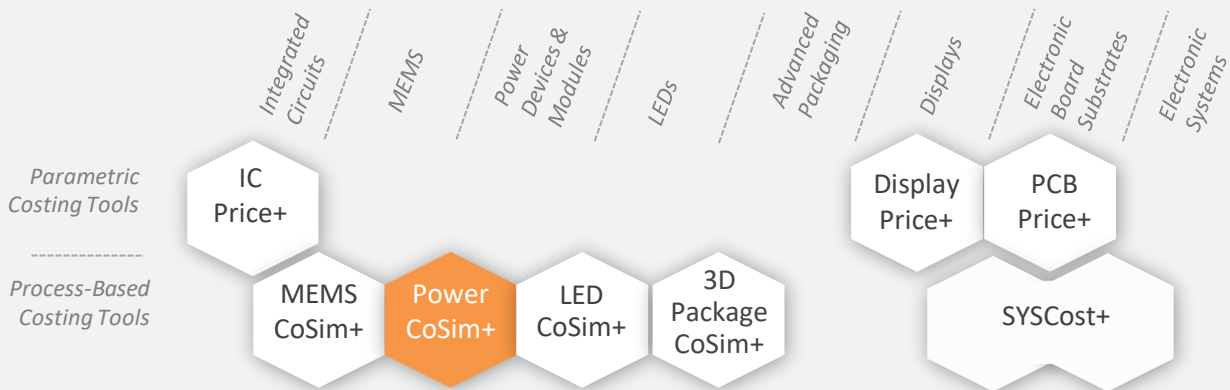


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