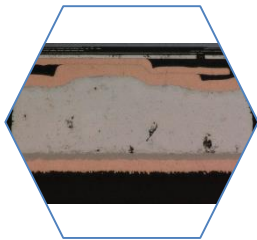


Advanced System-in-Package Technology in Apple's AirPods Pro

Analysis of Apple's first SiP found in the latest AirPods, featuring a fully integrated SiP for audio codec and Bluetooth connectivity.



The System-in-Package (SiP) market delivered huge revenue – \$13.4 billion – in 2019, and is expected to reach approximately \$18.8 billion in 2025. The market is mainly driven by increased need for advanced architectures in electronic devices, mostly in mobile and consumer products. Advanced technology asks for higher levels of die and functionality integration in a single package at lower cost. Since 2015, Apple has integrated several generations of SiP in its smartwatch. This year, for the first time, the company has chosen the same type of solution for its earbuds. This came in two different SiPs, one for the Bluetooth connectivity and one for the audio codec.

In the latest AirPods Pro, the SiPs influence device compactness and size reduction of the wireless headsets. The AirPods Pro, designed and manufactured by Apple, comprise several SiPs assembled together: two Inertial Measurement Units (IMUs), one Bluetooth module and one audio codec module. The IMUs are standard Land Grid Array (LGA) SiPs from STMicroelectronics.

The Bluetooth H1-Module is packaged using double side molding technology in order to integrate a memory under the System-on-Chip (SoC). This structure enables a wireless connection, drives Apple's voice-enabled Siri assistance and allows real time noise cancellation. The

audio codec integrates up to eight dies and 80 passive components with a density of 0.96 components per mm². The module has a special shape that is designed to meet the mechanical constraints of the earbuds to minimize the lost area in the system. Both SiPs are designed in order to have better power management, higher performance and high cost effectiveness.

The report includes all the packaging details from the substrate to the dies from both modules. The report focuses on the packaging processes of the two SiP modules and the final assembly. High resolution images of the package cross section at different positions and angles enable full package and assembly process analyses. It also includes a full description of the process and the manufacturing cost of the dies and the packaging. Finally, a physical comparison of the two SiP Modules is included.

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COMPLETE TEARDOWN WITH

- Detailed photos
- Precise measurements
- Material analysis
- Manufacturing process flow
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- Manufacturing cost analysis
- Estimated sales price

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AUTHORS

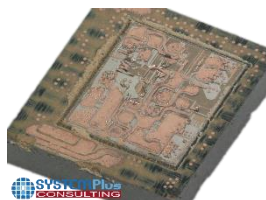


Belinda Dube is working for System Plus Consulting as an Engineer & Analyst, Semiconductor Memories. With an expertise focused on Micro & Nano Electronics, Belinda graduated from INSA (Lyon, France) with a master degree in Instrumentation & Nanotechnology Engineering.



Dr. Youssef El Gmili has joined System Plus Consulting's team in 2019 after ten years passed on high level research and development on microelectronics. He has a deep knowledge in the study of semiconductors Materials. He holds a Phd in Physics/Materials Science.

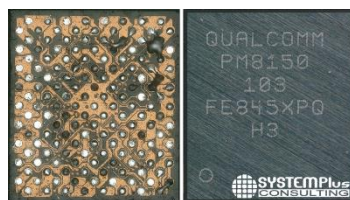
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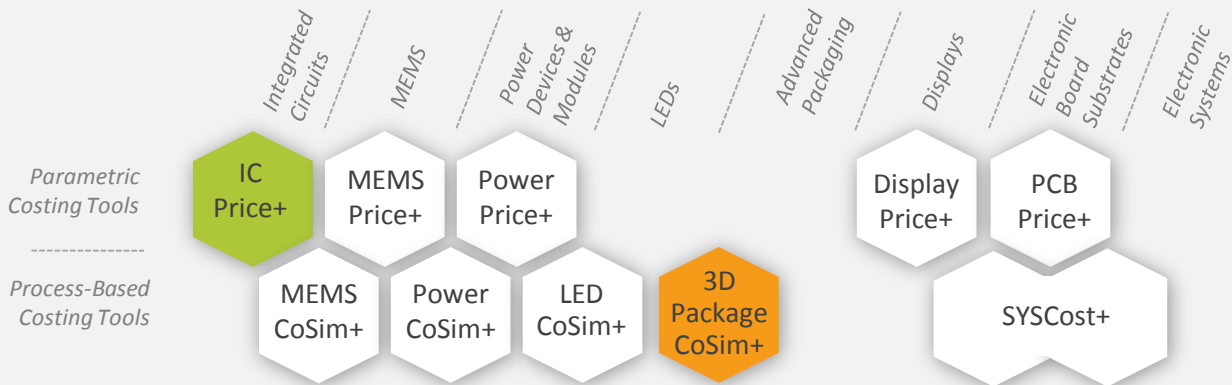


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January 2019

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ABOUT SYSTEM PLUS CONSULTING

WHAT IS A REVERSE COSTING®?

Reverse Costing® is the process of disassembling a device (or a system) in order to identify its technology and calculate its manufacturing cost, using in-house models and tools.



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