

Structure Analysis Huawei P20 Pro Teardown and Identification of Key Components

Discover Huawei's key devices, advanced packaging technical choices and main suppliers

Part No.	Manufacturer	Reference	Block / Key Function	IC / Feature	Package Type	Min. of Size	Package Ids / Code	Board	Full Part Name	Pin Count	Lead Type	Package Form	Package Footprint	Manufacturer	Package Material
7360-2A 47258-1 7803 M1	Roehif	...	Battery Charge Management	IC	DFN	6	32617	MidBoard-Top-Sls	Battery Charge Management	6	0.5	DFN	6x6	Roehif	MOBAP
TH W82	Roehif	...	Battery Charge Management	IC	DFN	6	44623	MidBoard-Top-Sls	Battery Charge Management	6	0.5	DFN	6x6	Roehif	MOBAP
A36	Roehif	...	Battery Charge Management	IC	DFN	6	21681	MidBoard-Top-Sls	Battery Charge Management	6	0.5	DFN	6x6	Roehif	MOBAP
ZGAD J1D	5A-Dual Charge with Advanced Safety Monitoring	IC	VIP-Pack	6	120006	MidBoard-Top-Sls	5A-Dual Charge with Advanced Safety Monitoring	6	0.5	VIP-Pack	6x6	...	VIP-Pack-IC
NFBL J3J	IC	...	41	...	MidBoard-Top-Sls

Title: Huawei P20 Pro Teardown
Pages: 50+
Date: May 2018
Format: PDF & Excel file
Price: EUR 1,490

COMPLETE TEARDOWN WITH:

- Smartphone teardown
- IC identification including marking, manufacturer, reference, function, package type, size and pin count
- PCB characteristics including cross-section and minimum line width
- Component supplier, package and footprint breakdown graphs

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 - ✓ IC package footprint

Through a teardown of the Huawei P20 Pro, this report details more than 100 integrated circuit (IC) devices used in the cellphone's main boards.

It will help you to identify the manufacturer and packaging, including its size, type and pitch, and function of the P20 Pro's main ICs. A physical analysis of the main substrates highlights internal structures and technologies used for the printed circuit boards (PCBs).

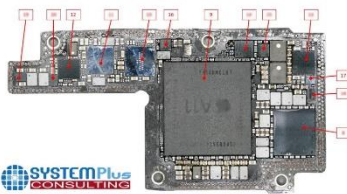
The report includes teardown photos, detailed package identification and descriptions and is supplied with an Excel file summarizing the P20 Pro chipset and breaking it down by supplier, package or footprint.

AUTHOR:



Benjamin Pussat has joined System Plus Consulting as a cost technician in 2017. He is in charge of reverse costing with a focus on boards and systems. He previously worked for Sercel, Sagem S&D and SEIA where he was in charge of boards test then HW and SW development. He holds a Bachelor's degree in electronics from the University of Nantes.

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