

LED

INTEGRATED CIRCUIT

MEMS

IMAGING

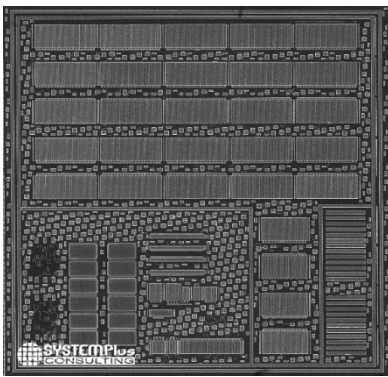
PACKAGING

SYSTEM

POWER

Texas Instruments DRA726 Jacinto 6 Eco SoC Processor for Automotive Infotainment

Cost-optimized in-vehicle infotainment for entry- to mid-level automobile segments



Infotainment in automobiles is growing very quickly as people require ever more in-vehicle experiences. It is now one of the most important factors when choosing to buy a new car. Enhanced functions have become ordinary in the high-end car segment, but there is now a strong pressure to integrate the same kind of interfaces in entry-level and mid-level vehicles.

Texas Instruments (TI), with the Jacinto 6 Eco processor family, addresses these entry- to mid-level segments by providing a cost effective solution with feature-rich in-vehicle infotainment.

The DRA726 is a System-on-Chip (SoC) able to deliver high-integrity audio, simultaneous multimedia streaming and device connectivity. It includes an ARM Cortex-A15 microprocessor unit (MPU), a C66x digital signal processor (DSP), two ARM Cortex-M4 MPUs and many automotive interfaces to provide a cost-optimized bill-of-material (BOM) for tier one vehicle manufacturers.

The SoC is based on enhanced OMAP™ architecture integrated on a 28nm technology and is provided in a 760-ball ball grid array (BGA) package featuring TI's Via Channel Array (VCA) technology. It is AEC-Q100 qualified with automotive grade 1 temperature specifications, from -40°C to +125°C.

The report provides a complete physical analysis of the DRA726 component with key information on the packaging and the die, including size, process used and main block identification. It also provides an estimation of its manufacturing cost and selling price with forecasts for coming years.

Title: TI DRA726 SoC Processor

Pages: 60

Date: October 2016

Format: PDF & Excel file

Price: Full report: EUR 2,490

COMPLETE TEARDOWN

WITH:

- Detailed photos
- Precise measurements
- Material analysis
- Manufacturing process
- Supply-chain evaluation
- Manufacturing cost analysis
- Estimated sales price based on quantity and year

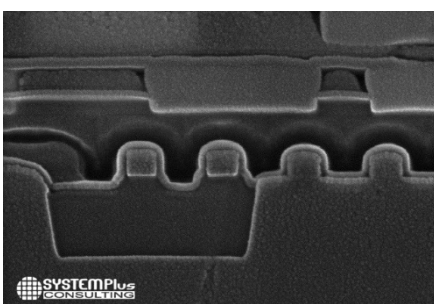


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Romain is in charge of costing analyses for MEMS, IC and Advanced Packaging. He has published more than 50 Reverse Costing reports on various MEMS devices including Inertial, Pressure, Microphones or RF Sensors.



Nicolas Radufe (Lab)

Nicolas is in charge of physical analysis. He has a deep knowledge in chemical and physical analyses. He previously worked in microelectronics R&D for CEA/LETI in Grenoble and for STMicroelectronics in Crolles.

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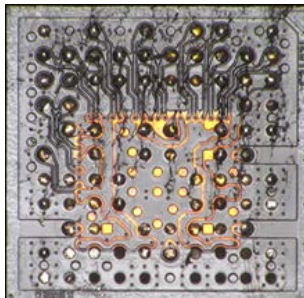
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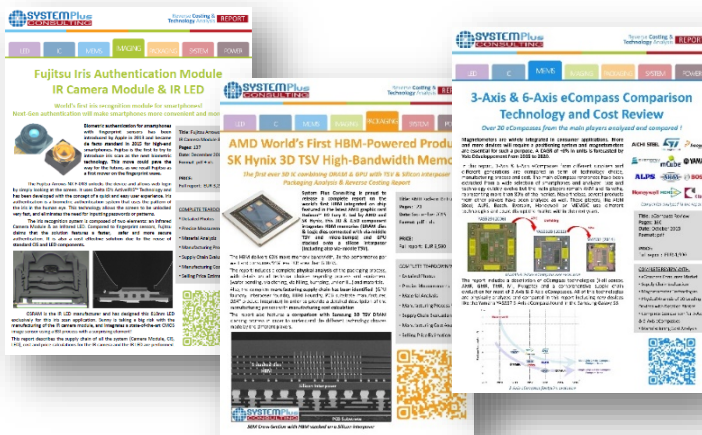
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