The combination of GaN Systems high voltage HEMT and AT&S ECP Embedded die package is enabling optimized heat management, package thickness and high power performance at high frequency.

The GS66508P from GaN Systems is a GaN on Silicon HEMT transistor packaged in a new GaNpx Embedded Die package. It features a high-voltage breakdown voltage of 650V for a current of 30A (25°C), with lower switching losses. The transistor is optimized for AC-DC converters and high frequency, high efficiency power conversion.

The GS66508P is packaged in an innovative embedded die package developed by AT&S (ECP process). This package has no wire bonding to reduce the inductance and a design to increase the heat management using the two sides of the die. The GaN and AlGaN layers are deposited by epitaxy on a silicon substrate. A complex buffer and template layers structure is used to reduce the stress and the dislocation. This is completed by a thick superlattice structure clearly visible in the TEM analysis.

This full reverse costing analysis has been conducted to provide insight on technology data, manufacturing cost and selling price of the GaN Systems transistor and the Embedded Die Package of AT&S.
Please process my order for “GaN Systems GS66508P” Reverse Costing Report

Ref.: SP15202

- Full Reverse Costing report: EUR 2,990*
- Technology Analysis** report: EUR 1,990

*For price in dollars please use the day's exchange rate  
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** Technology Analysis report contains only Physical Analysis & Manufacturing Process Flow

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